

You should test in the following sequence:

1. Install the latest version of LLC Toolkit,
2. In LabVIEW program go to the "Help" menu than choose "Find Examples" and search the words "LLC" or "LossLess Compression",
3. In the example list find "LLC Host VI.vi" and open it,
4. In the opened window set "Pulse Duration in Samples" as 1000 , "Negative Side Duration in Samples" as 7000,
5. Run the "LLC Host VI.vi",
Note: The default bit file is selected for PXIe-7966R (if you want to change bit file for your PXIe follow these steps)
 - 5.1 In the "LLC Host VI.vi" open menu "Window" and choose "Show Block Diagram",

 - 5.2 In the block diagram find "Open FPGA Reference" icon (it should have PXIe-7966R text in it) , right click on it and choose "Configure Open FPGA Reference",

 - 5.3 Click on the "Browse" button and choose the bit file that fits with your PXIe (the bit files directory is "C:/Program Files(x86)/National Instruments/LabVIEW 2014/vi.lib/Olymp Engineering LLC/LossLess Coding Library"),
6. Watch the results on the "Time Signal to Coding" and "Signal After Coding and decoding with source signal" graph indicators.